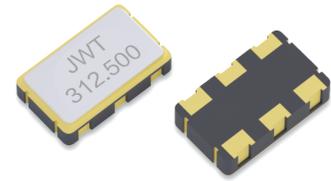


# 2.0x1.6 mm DYN Series

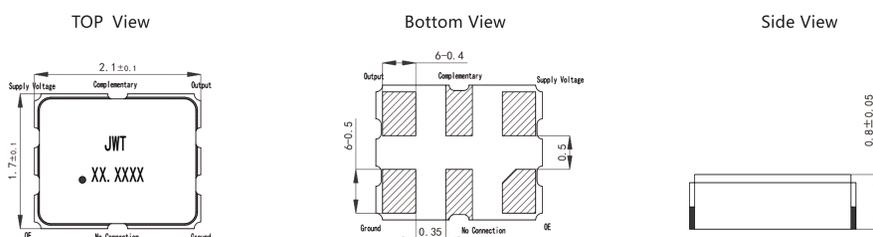


## Specifications

Model	YN6312M500332015P	Conditions / Remarks	
Output type	LVPECL		
	3.3V		
Frequency Range	312.500MHz		
Oscillation Mode	Fundamental		
Frequency Stability	±20ppm	Includes initial frequency tolerance, temperature variation, supply voltage change and 10 years aging (+25 °C)	
Operating temperature range	-40°C~105°C		
Storage temperature range	-55°C~125°C		
High-level output voltage	VDD-1.1 MIN		
Low-level output voltage	VDD-1.5 MAX		
Current Consumption	60mA (max)	RL=50Ω to VDD-2V	
Rise Time	0.4ns	Between 20 % and 80 % of (VOH - VOL)	
Fall Time	0.4ns		
Symmetry	45 % to 55 %		
Differential swing	0.8 V to 2.0 V		
Start-up Time	10ms	t = 0 at 90 % VCC	
Output Load	50Ω	Terminated to VCC - 2.0 V	
Tri-State	Output Enable	70%VDD Min	Output will be enable if OE is Logic 1 or open ; Output will be disable if OE is Logic 0.
	Output Disable	30%VDD Max	
RMS Phase Jitter	35fs(typ)	Integrated from 12KHz ~ 20MHz @312.5MHz , 3.3V ,	

Notes: The above parameters are for normal demands, if you have any different demands, please check with us.

## Dimensions (Unit: mm)



Pin	Function
#1	CE or NC
#2	NC
#3	GND
#4	OUT
#5	OUTN
#6	VDD